

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of:
Deshpande et al.

Atty. Docket No.: FIS920030397US1

Serial No.: 10/709,048

Group Art Unit: 2814

Filed: April 8, 2004

Examiner: Ingham, John C.

For: A MANUFACTURABLE METHOD AND STRUCTURE FOR DOUBLE SPACER
CMOS WITH OPTIMIZED NFET/PFET PERFORMANCE

AMENDMENT UNDER 37 C.F.R. §1.116

Mail Stop Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This amendment is in response to the Office Action mailed September 12, 2007, setting a three-month statutory period for response. Therefore, this amendment is timely filed. Please amend the above-identified patent application as follows: